IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Rawlins et al.

Appl. No.: 10/719,058

Filed: November 24, 2003

For: Methods, Systems, and Computer

Program Products for Parallel Correlation and Applications

Thereof

Confirmation No.: 9124

Art Unit: 2193

Examiner: David H. Malzahn

Atty. Docket: 1744.1380000

Seventh Supplemental Information Disclosure Statement

Mail Stop Amendment

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

Listed on accompanying IDS Forms are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Applicants have listed publication dates on the attached IDS Forms based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

Applicants provide the following comments regarding the documents:

Document US1 was cited in a Notice of Allowance in related U.S. Patent Application No. 11/020,547, filed December 27, 2004, entitled "Methods And Systems For Down-Converting A Signal Using A Complementary Transistor Structure," directed to related subject matter.

Documents US2-US4 were cited in an Office Action in related U.S. Patent Application No. 10/197,255, filed July 18, 2002, entitled "Networking Methods and Systems," directed to related subject matter. Also cited in said Office Action were U.S. Publication Nos. 2002/0070728 and 2002/0163921, and U.S. Patent Nos. 6,963,626; 6,850,742; 6,591,310; and 5,703,584, all of which have already been cited in the present application.

Document US5 was cited in a Notice of Allowance in related U.S. Patent Application No. 10/453,622, filed June 4, 2003, entitled "Active Polyphase Inverter Filter for Quadrature Signal Generation," directed to related subject matter.

Documents US6-US14 are co-owned patents which are directed to related subject matter.

Documents US15 and US16 were cited in a Notice of Allowance in related U.S. Patent Application No. 10/914,337, filed August 10, 2004, entitled "Method And System For Down-Converting And Up-Converting An Electromagnetic Signal, And Transforms For Same," directed to related subject matter. Also cited in said Notice of Allowance were U.S. Patent Nos. 4,761,798; 6,144,846; 6,400,963; 6,611,569; 6,836,650; 6,853,690; 6,873,836; and 7,194,246, all of which have already been cited in the present application.

Documents US17-US19 were cited in an Office Action in related U.S. Patent Application No. 11/355,167, filed February 16, 2006, entitled "Methods And Systems For Down-Converting A Signal Using A Complementary Transistor Structure," directed to related subject matter.

Documents US20, US21, and FP4-FP8 were cited in an Office Action in Israeli Patent Appl. No. 145,908, filed April 14, 2000, entitled "Device for Frequency Down-Converting Using Undersampling, and Method," directed to related subject matter.

Documents US22-US24 were cited in a Notice of Allowance in U.S. Patent Appl. No. 10/305,299, filed November 27, 2002, entitled "Method and Apparatus for Improving Dynamic Range in a Communication System," directed to related subject matter.

Documents US25 and US26 were cited in a Notice of Allowance in U.S. Patent Appl. No. 10/453,622, filed June 4, 2003, entitled "Active Polyphase Inverter Filter for Quadrature Signal Generation," directed to related subject matter.

Documents FP1-FP3 were cited in an Office Action in Japanese Patent Appl. No. 2000-577764, filed October 18, 1999, entitled "Applications of Universal Translation," directed to related subject matter.

This statement should not be construed as a representation that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicants have checked the appropriate boxes below.

- ☐ 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this
 Information Disclosure Statement was first cited in a communication from a
 foreign patent office in a counterpart application and this communication was not
 received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty
 days prior to the filing of this information disclosure statement.
- 2. Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed within three months of the date of filing of a national application other than a continued prosecution application (CPA), OR within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, OR before the mailing date of a first Office Action on the merits OR before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.
- - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than

three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- ∑ c. The required fee is provided through online credit card payment authorization in the amount of \$180.00 in payment of the fee under 37 C.F.R. § 1.17(p).
- 4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. The required fee is provided through online credit card payment authorization in the amount of \$180.00 in payment of the fee under 37 C.F.R. § 1.17(p); in addition:
 - a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than

three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).

- b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- 5. The document(s) was/were cited in a search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- A concise explanation of the relevance of the non-English language documents appears below in accordance with 37 C.F.R.
 § 1.98(a)(3):

Document FP1 (JP 58-031622) appears to describe a frequency converting circuit. An English abstract is enclosed as document NPL1.

Document FP2 (JP 61-245749) appears to describe a PCM signal transmission system for CATV. An English abstract is enclosed as document NPL2.

Document FP3 (JP 64-048557) appears to describe a circuit for reforming and modulating at least two data signals. An English abstract is enclosed as document NPL3.

Document FP4 (JP 59-022438) appears to describe a bipolar analog-frequency converting circuit. An English abstract is enclosed as document NPL4.

Document FP5 (JP 59-123318) appears to describe an analog-digital converting circuit. An English abstract is enclosed as document NPL5.

Document FP6 (JP 61-193521) appears to describe an analog-digital converting circuit. An English abstract is enclosed as document NPL6.

Document FP7 (JP 62-047214) appears to describe a digital-analog converting circuit. An English abstract is enclosed as document NPL7.

Document FP8 (JP 63-274214) appears to describe a digital/analog converting circuit. An English abstract is enclosed as document NPL8.

⊠ 7.	Copies of documents FP1-FP8 and NPL1-NPL8 are submitted. However, in
	accordance with 37 C.F.R. § 1.98(a)(2), no copies of U.S. patents and patent
	application publications cited on the attached IDS Forms are submitted.

8. Copies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No._______, filed _______, which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

∑ 9. It is expected that the examiner will review the prosecution and cited art in the parent application no. 09/987,193, filed November 13, 2001 (now U.S. Patent No. 7,010,559), in accordance with MPEP 2001.06(b), and indicate in the next communication from the office that the art cited in the earlier prosecution history has been reviewed in connection with the present application.

It is respectfully requested that the Examiner initial and return a copy of the enclosed IDS Forms, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

RNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

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Date: 10/12/67

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